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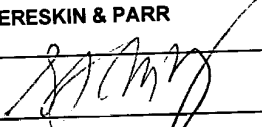
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/854,905
	Filing Date	May 15, 2001
	First Named Inventor	MOORE, Brian
	Group Art Unit	2829
	Examiner Name	
Total Number of Pages in This Submission	Attorney Docket Number	11157-23

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	BERESKIN & PARR
Signature	 Stephen M. Beney, Reg. No. 41,563
Date	September 25, 2002

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September 25, 2002

Stephen M. Beney B.Sc. (Physics)
416 957 1697 sbeney@bereskinparr.com

#14 IDS

M. Brunson

Your Reference: 09/854,905
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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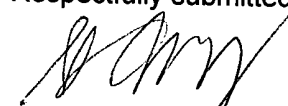
Dear Sir:

Re: Patent Application No. 09/854,905
For: WIRELESS RADIO FREQUENCY TECHNIQUE DESIGN AND METHOD
FOR TESTING OF INTEGRATED CIRCUITS AND WAFERS
Applicant: MOORE, Brian
Filed: May 15, 2001
Group: 2829

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In accordance with 37 CFR 1.56 and 1.97(b), applicant hereby submits a Supplemental Information Disclosure Statement and provides a copy of a technical paper that was cited in an International Preliminary Examination Report (IPER) that issued for the corresponding PCT application. The IPER issued on September 11, 2002. Form PTO-1449 is also enclosed. Please have this document recorded against the above-mentioned application.

Respectfully submitted,


Stephen M. Beney
Registration No. 41,563

TO/cec
Encl.

please send your reply to

Scotia Plaza, 40 King Street West, 40th Floor
Toronto, Ontario, Canada M5H 3Y2
t: 416 364 7311 f: 416 361 1398

Meadowvale Corporate Centre, 2000 Argentia Road
Plaza 4, Suite 430, Mississauga, Ontario, Canada L5N 1W1
t: 905 812 3600 f: 905 814 0031

www.bereskinparr.com